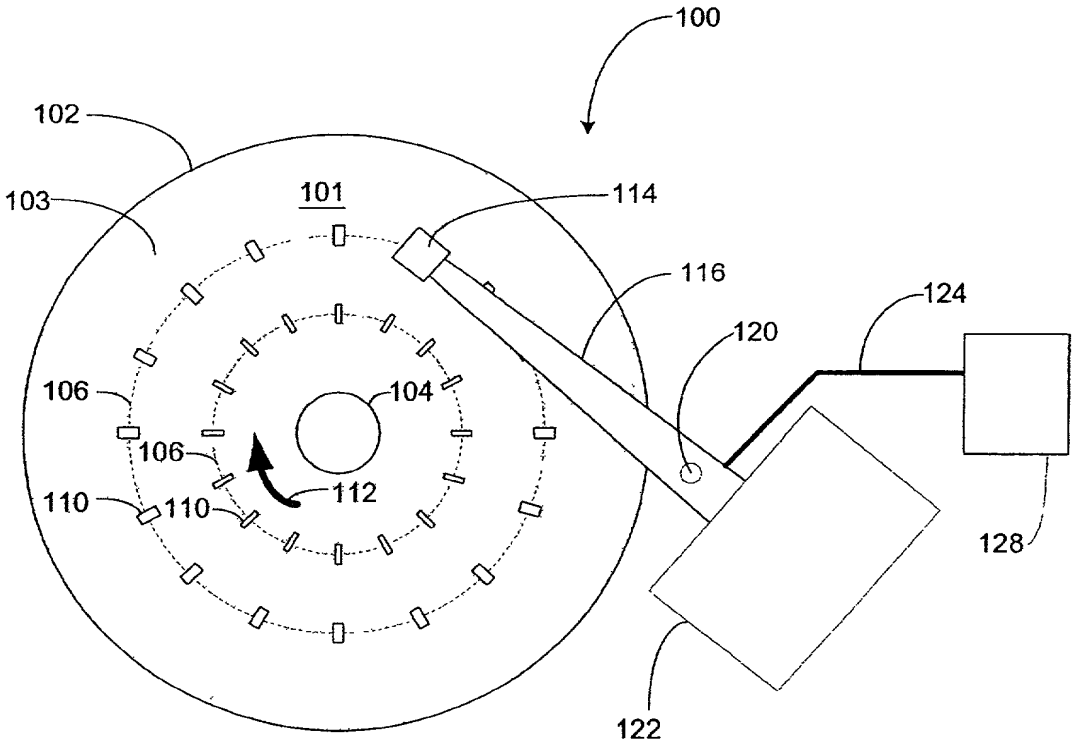

FIGURE 1A

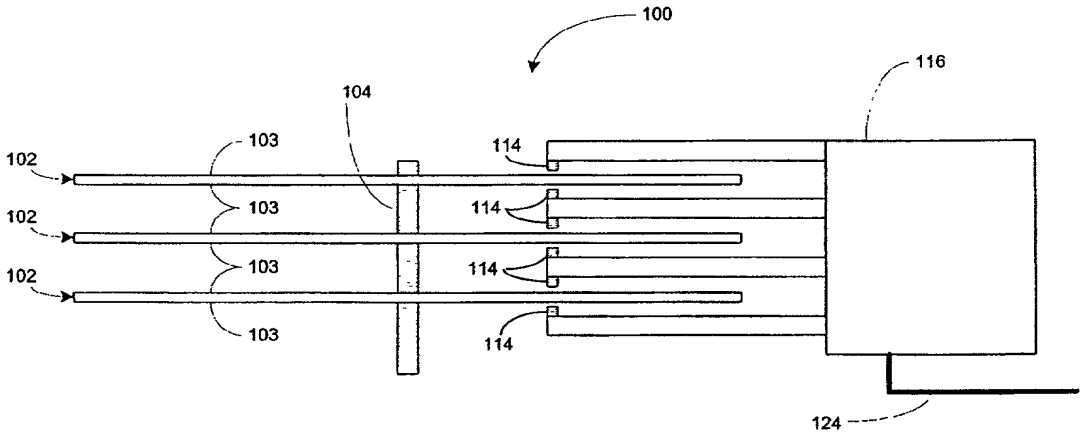

FIGURE 1B

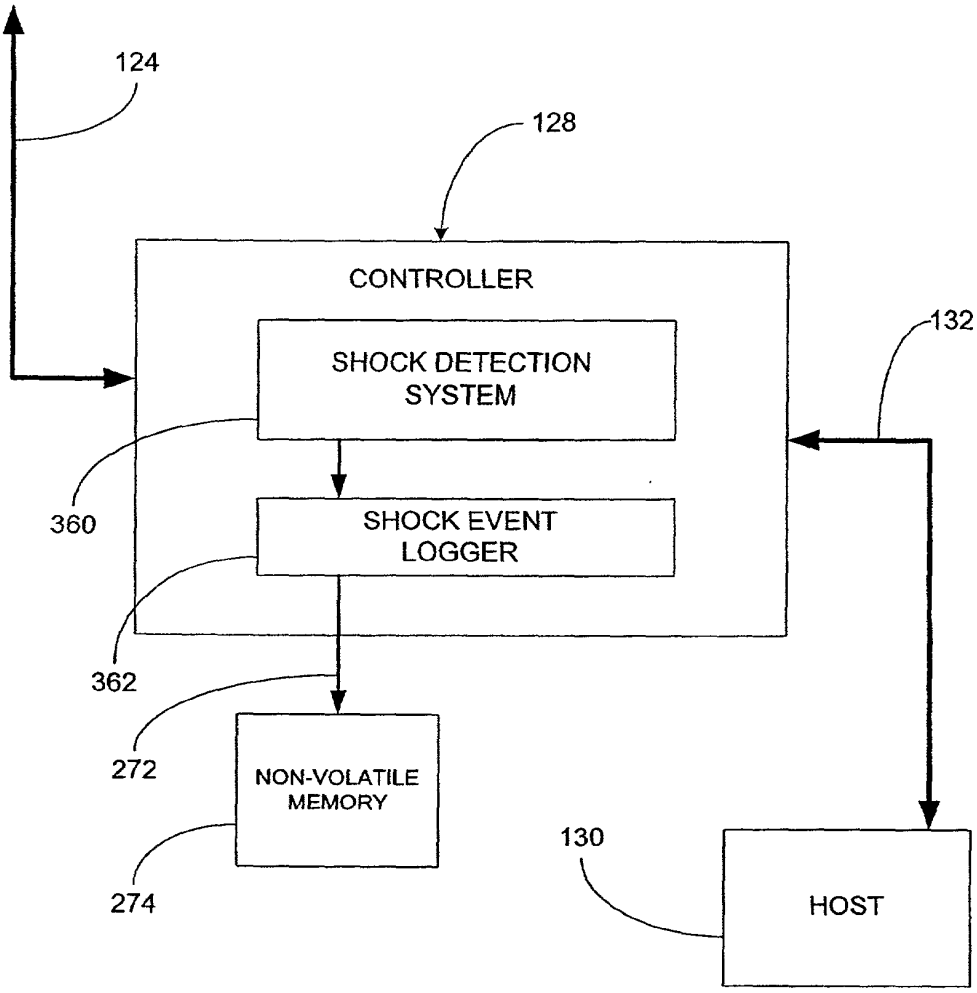
124
128
CONTROLLER
SHOCK DETECTION SYSTEM
132
360
SHOCK EVENT LOGGER
362
272
NON-VOLATILE MEMORY
274
130
HOST

FIGURE 2

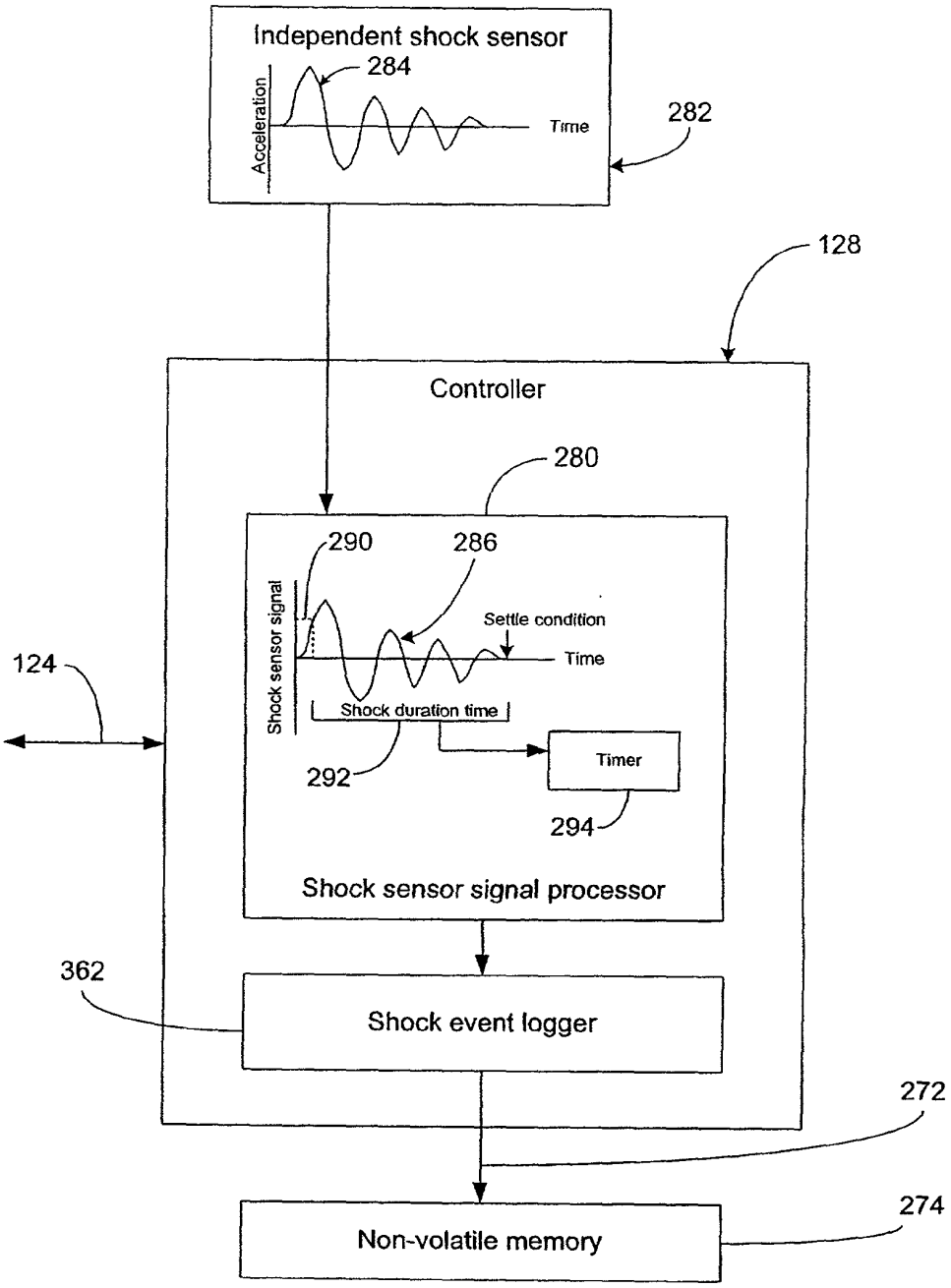

FIGURE 3A

Seagate Exhibit 1006
Page 37 of 56

Application Serial No. 10/008,763

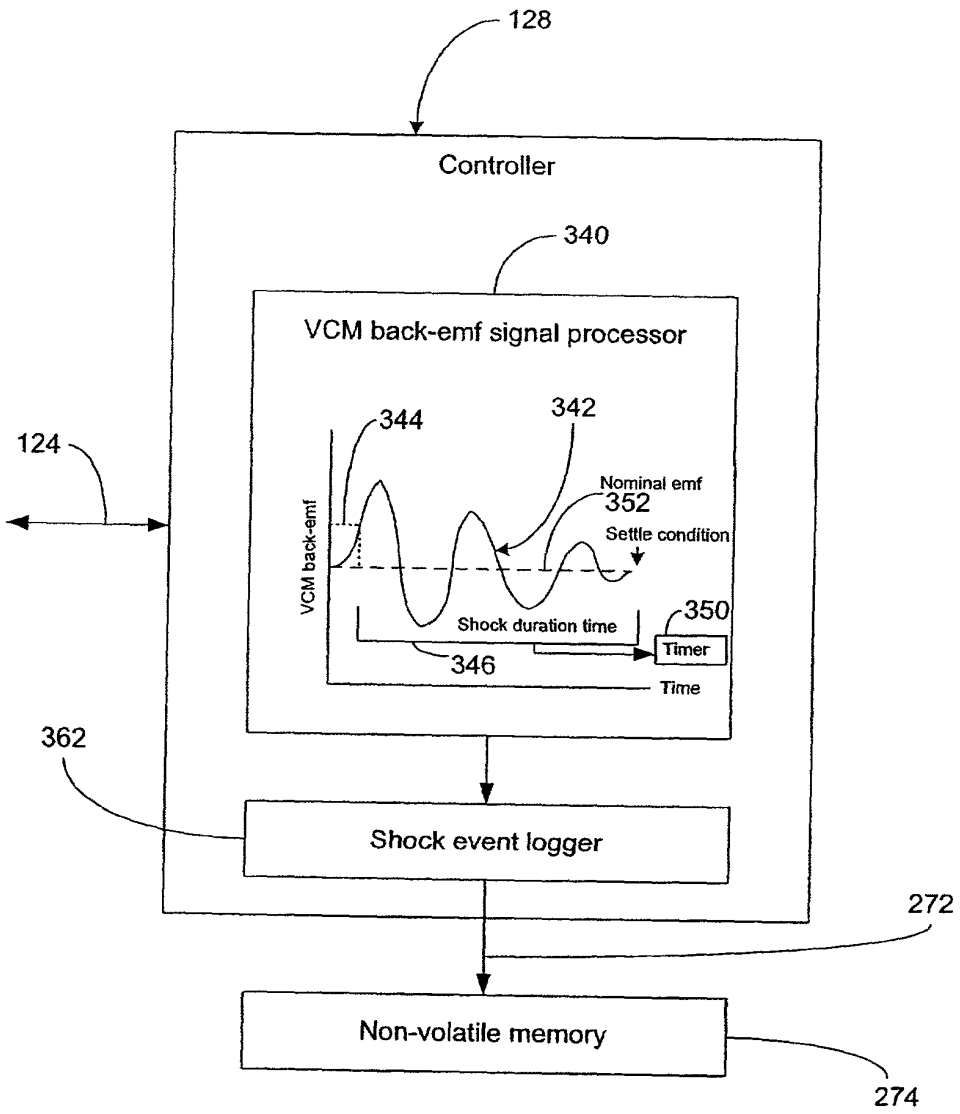

FIGURE 3B

Sep 15, 2004 9:49 AM

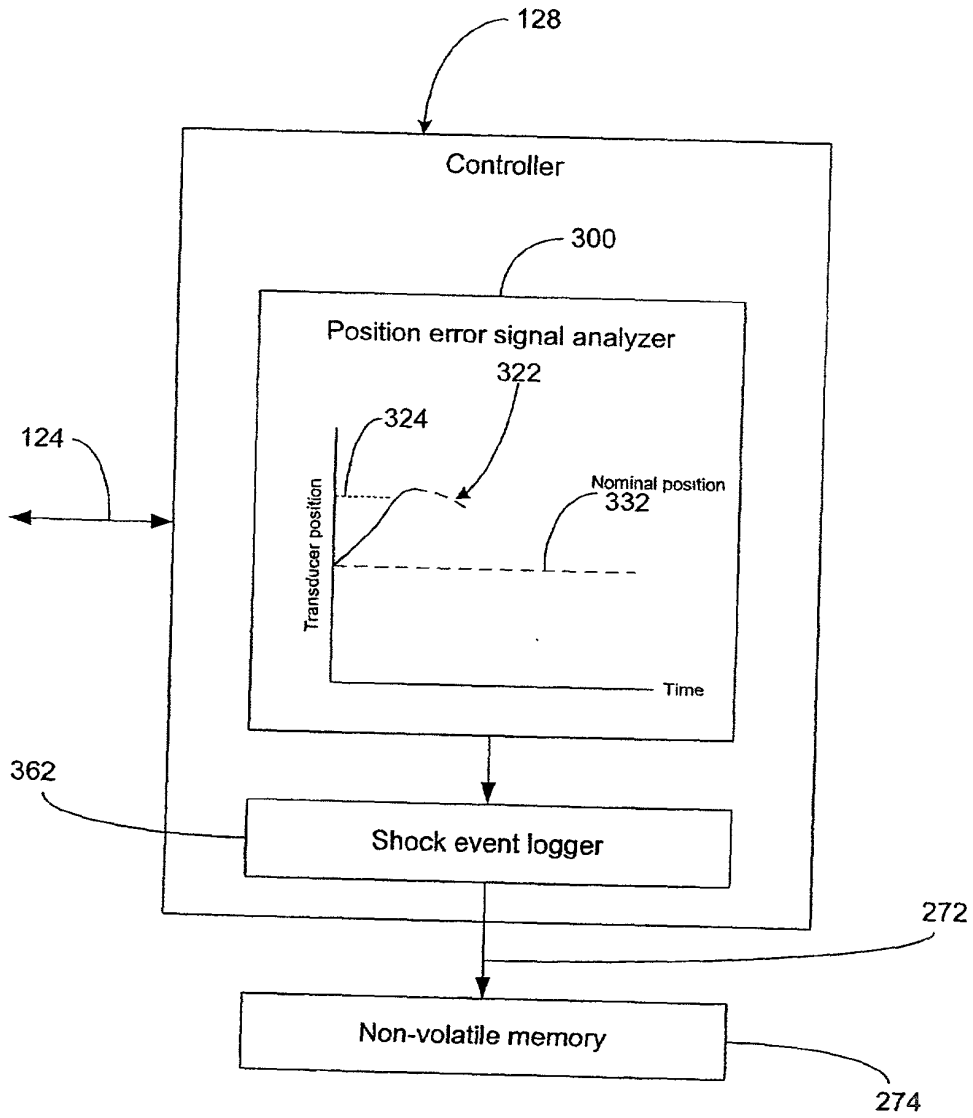

FIGURE 3C

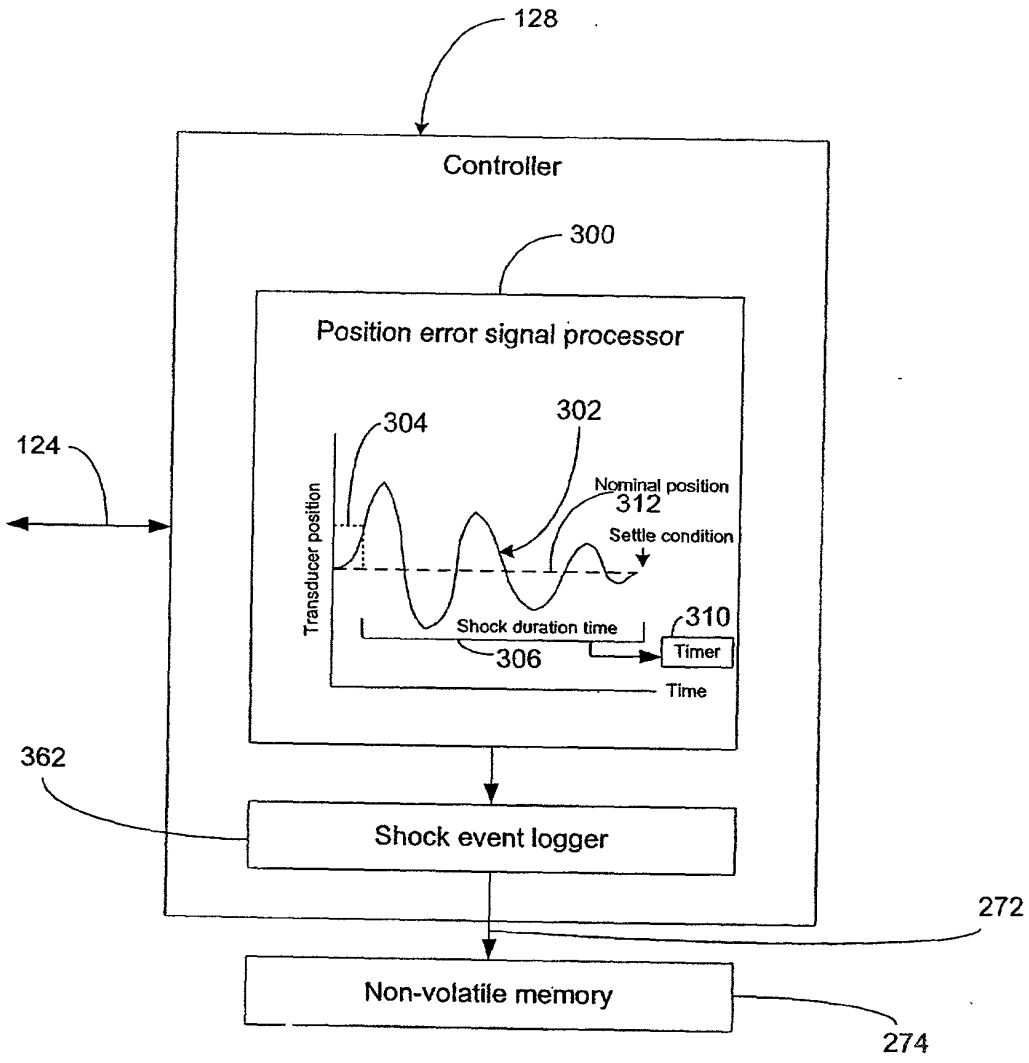
128
Controller
300
Position error signal processor
304
302
124
Nominal position
312
Settle condition
Transducer position
310
Shock duration time
Timer
306
Time
362
Shock event logger
272
Non-volatile memory
274

FIGURE 3D

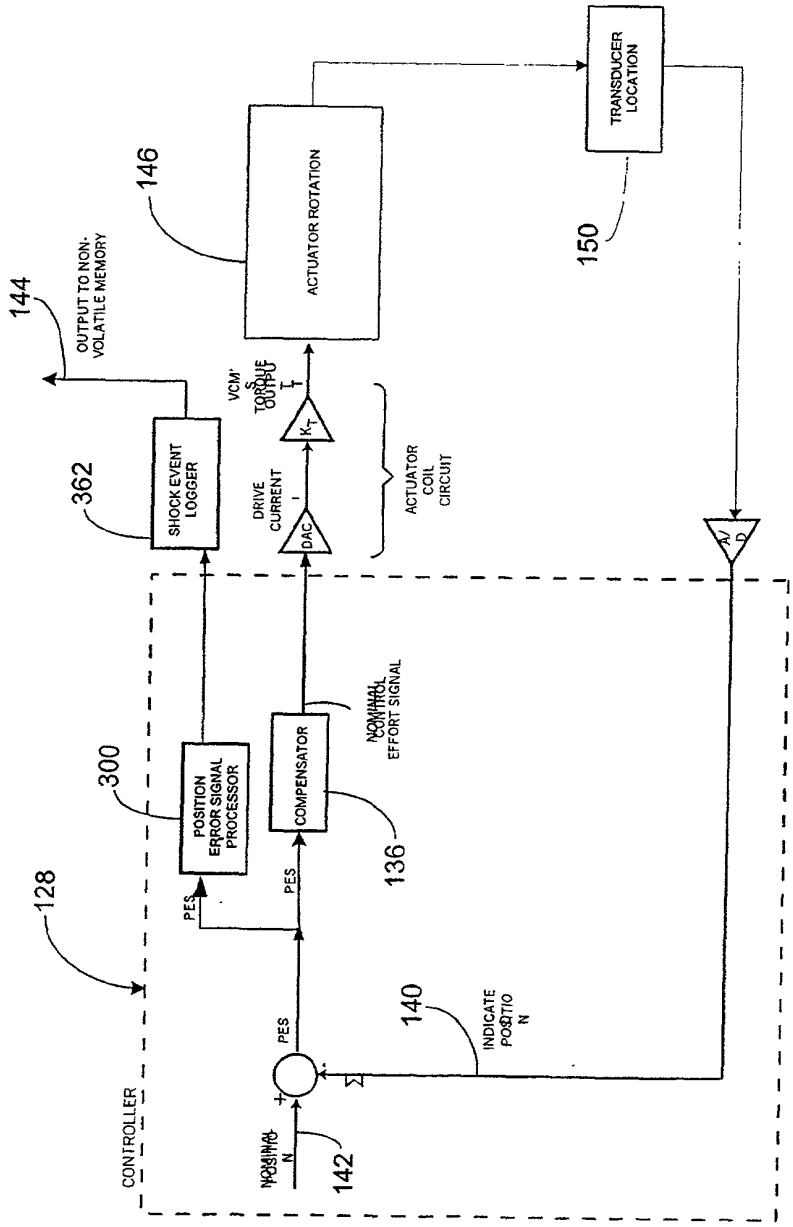
CONTROLLER
128
300
POSITION ERROR SIGNAL PROCESSOR
362
SHOCK EVENT LOGGER
144
OUTPUT TO NON-VOLATILE MEMORY
136
COMPENSATOR
NOMINAL CONTROL EFFORT SIGNAL
DRIVE CURRENT
DAC
VCM TORQUE OUTPUT
K_T
ACTUATOR COIL CIRCUIT
146
ACTUATOR ROTATION
TRANSDUCER LOCATION
150
A/D
140
INDICATE POSITION
142
PES
PES
PES
+
-
Σ

FIGURE 4

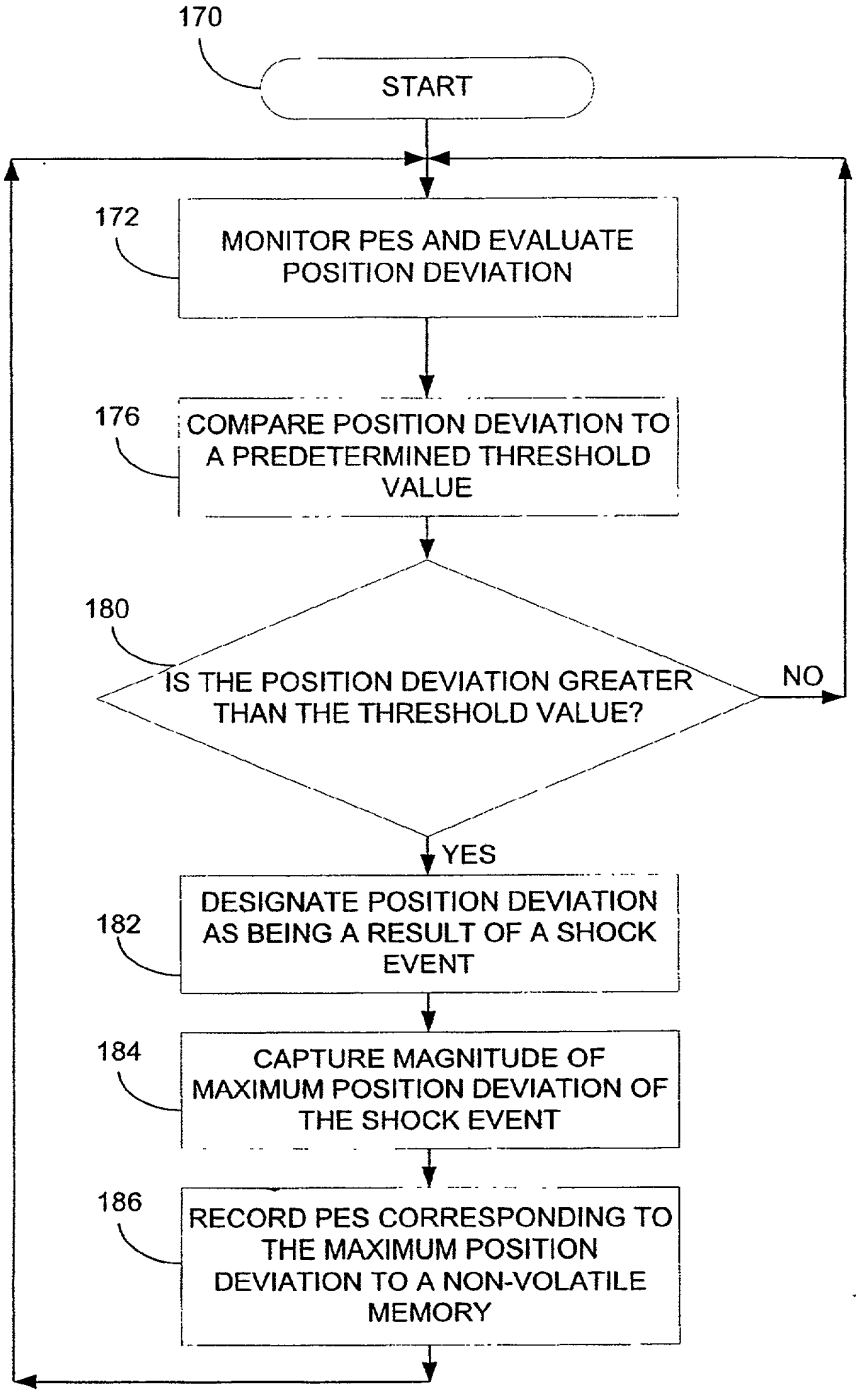

FIGURE 5

Attorney Docket No. 50277-2000

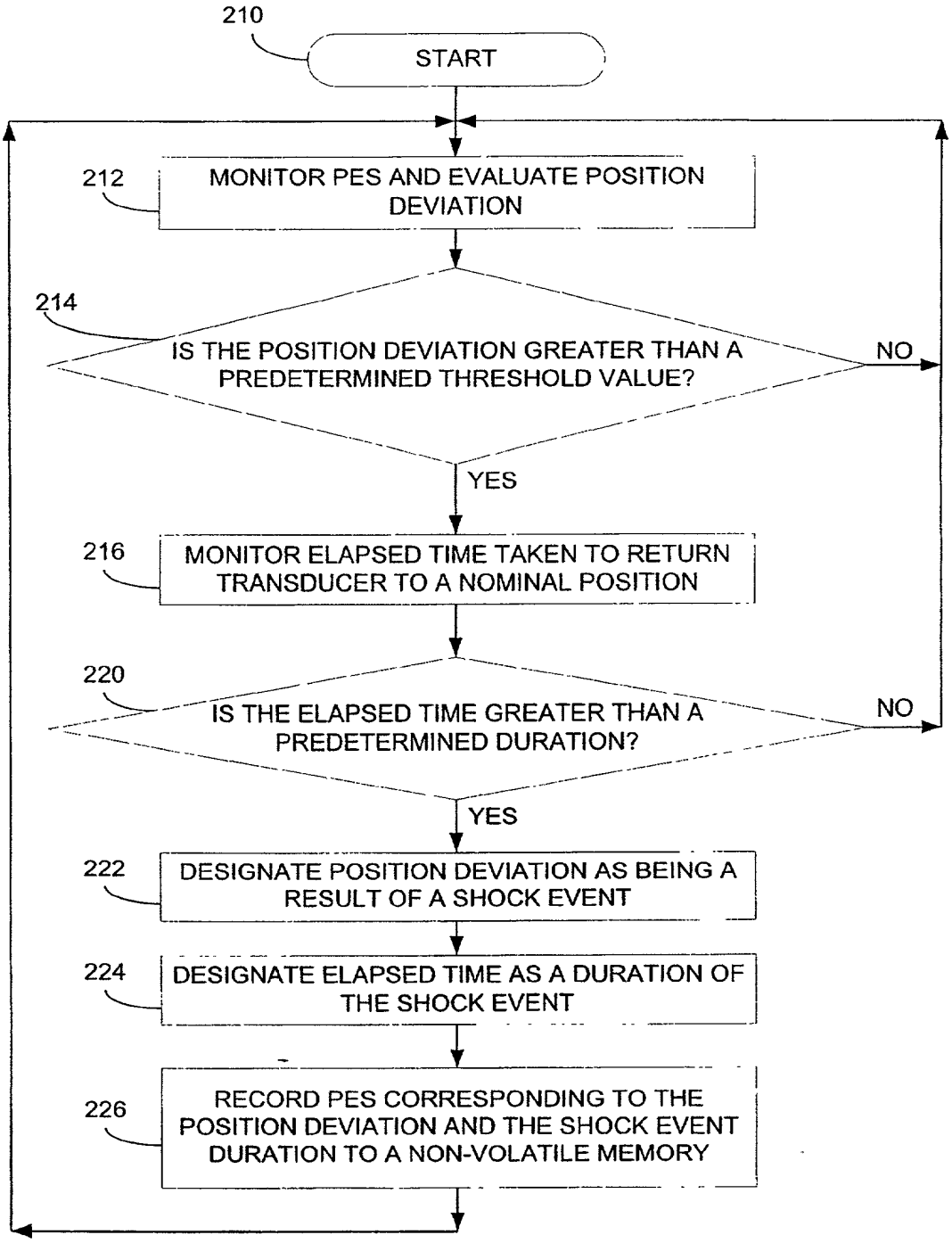

FIGURE 6

Exhibit 1005, p. 13 of 18

Docket No. SJO920010071US1

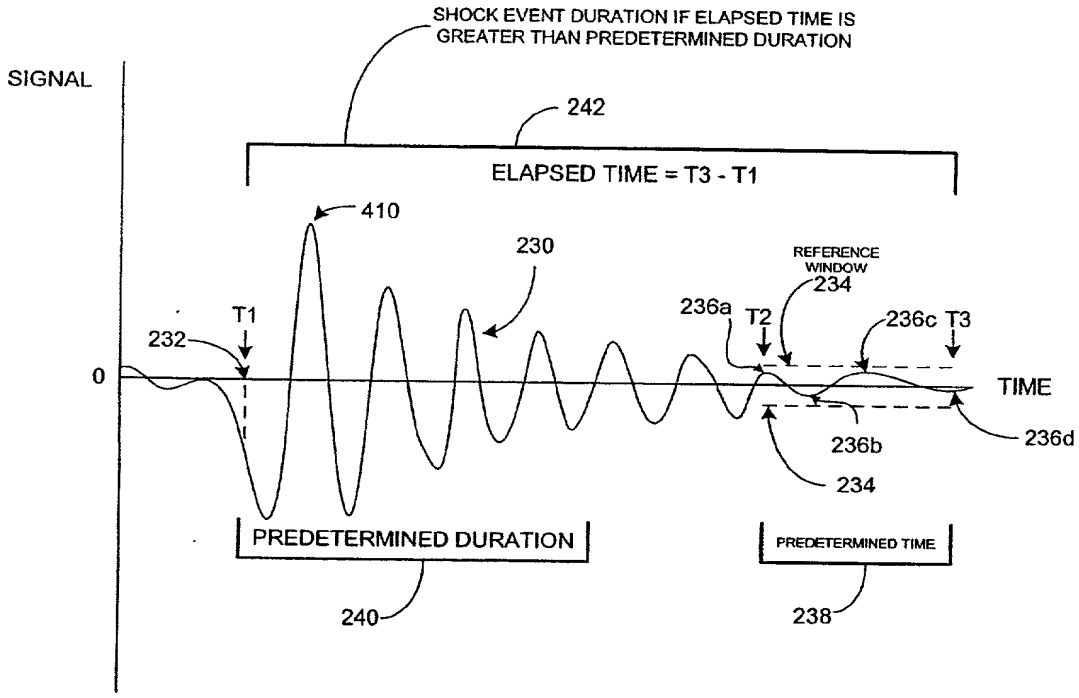

FIGURE 7

Patent Application Publication

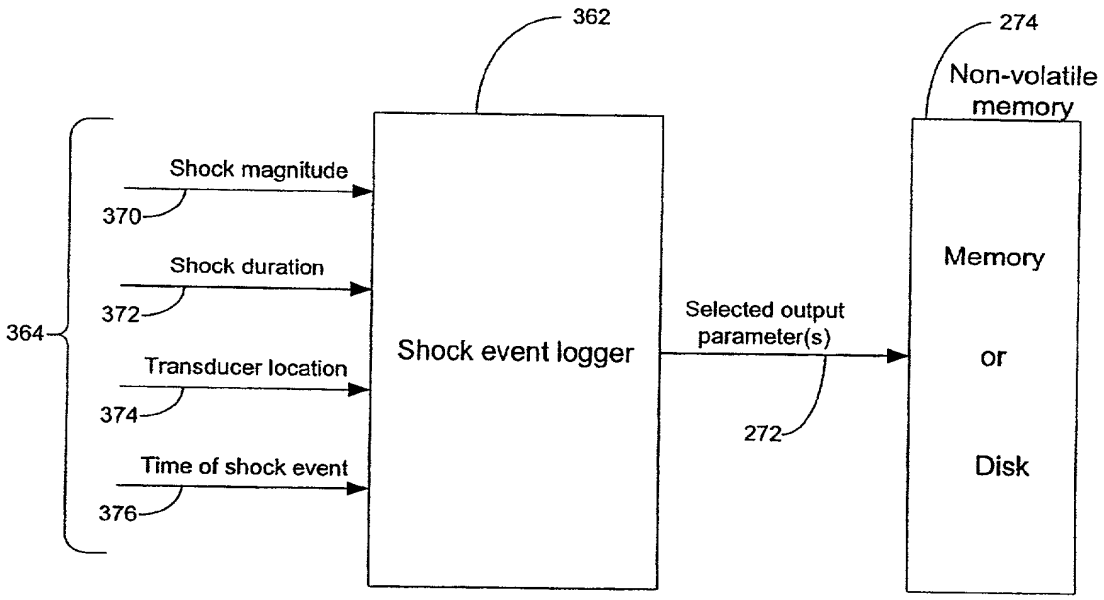

FIGURE 8

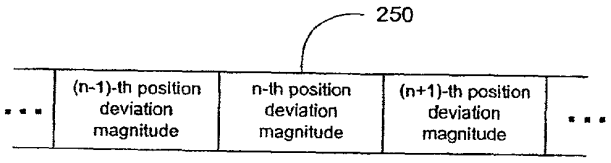

FIGURE 9A

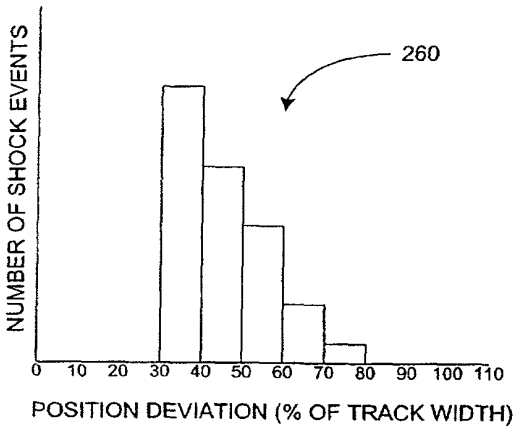

FIGURE 9B

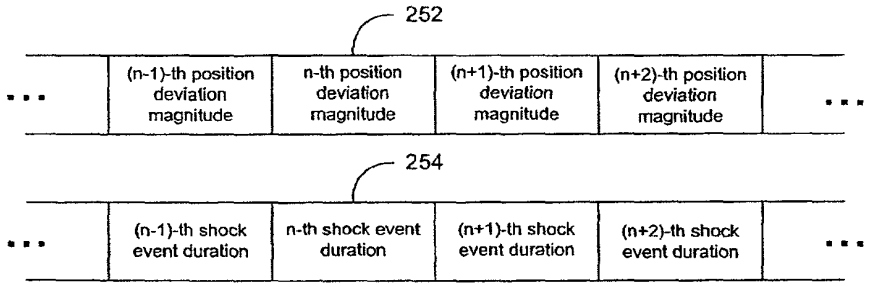

FIGURE 9C

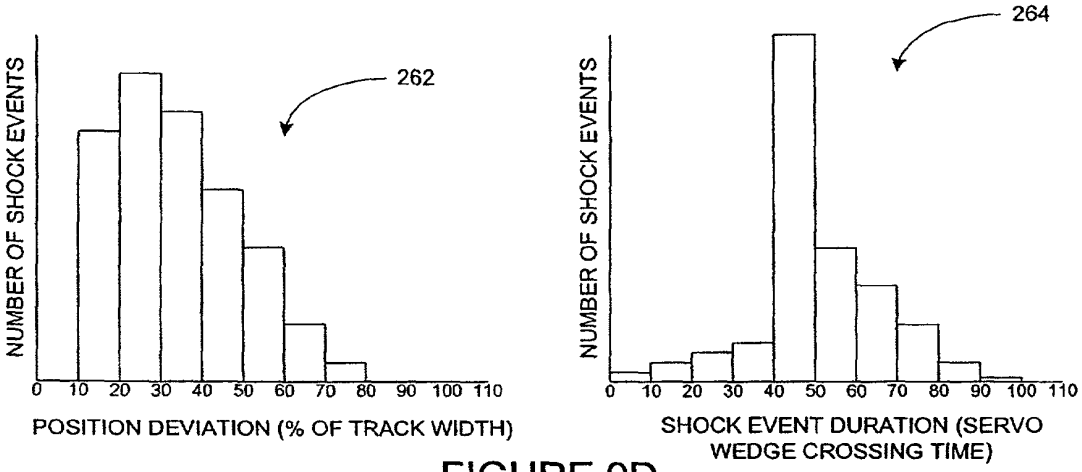

FIGURE 9D

Attorney Docket No. 90104.0001

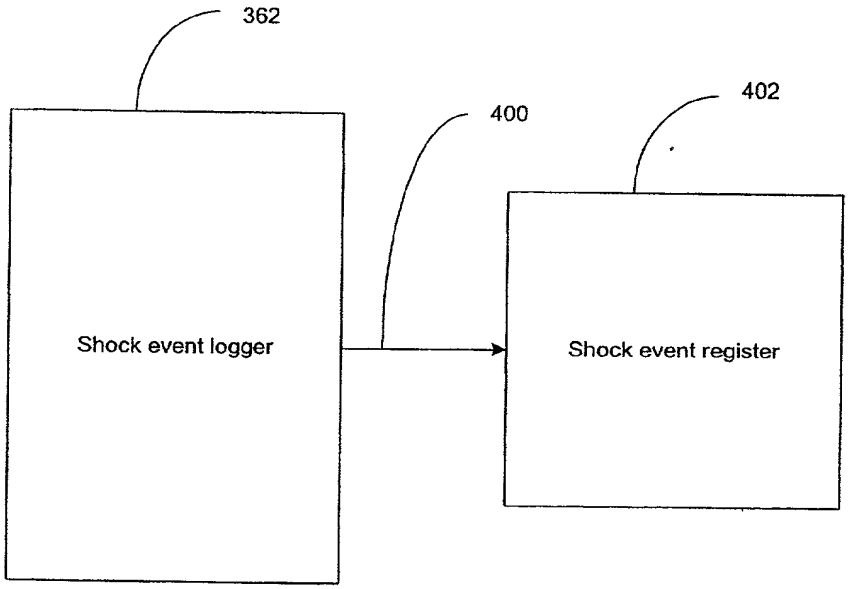

FIGURE 9E